

07/06/2010

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§ Art Unit:	2195
Hong Jiang et al.	§ Examiner:	Eric Charles Wai
Serial No.:	§ Conf. No.:	8582
Filed:	§ Docket:	ITL.1704US P17510
For:	§ Assignee:	Intel Corporation
Processing Architecture Having Passive Threads and Active Semaphores		

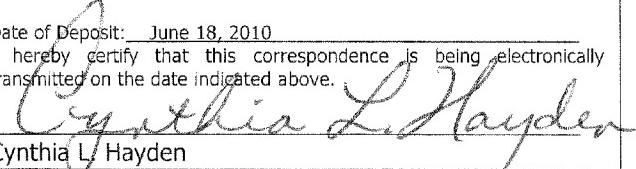
Mail Stop **AF**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed May 10, 2010, please amend the above-referenced patent application as follows:

Date of Deposit: June 18, 2010
I hereby certify that this correspondence is being electronically transmitted on the date indicated above.


Cynthia L. Hayden